

10/700,779.

COFC


TRANSMITTAL LETTER
 (General - Patent Issued)

 Docket No.
 N1085-90025

Patent(s) No. et al.

U.S. Patent No.

6,849,546 *bi*

Issue Date

2/1/2005

 Title: **METHOD FOR IMPROVING INTERLEVEL DIELECTRIC GAP FILLING OVER SEMICONDUCTOR
 STRUCTURES HAVING HIGH ASPECT RATIOS**
COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Certificate of Correction

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Dated: 3/22/2006

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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Susan Barlett

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APR 03 2006

P17A/REV04

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,849,546 *B1*

DATED : 2/1/2005

INVENTOR(S) : Tu et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 3, column 5, line 28, delete "devises" and insert --devices--.

Claim 4, column 5, line 29, delete the first "a" and insert --are--.

Claim 13, column 5, line 63, delete "gas" and insert --gaps--.

Claim 23, column 6, line 48, delete "silicion" and insert --silicon--.

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PATENT NO. 6,849,546

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